

Preliminary Amendment

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and illustrated, and in their operation, and of the methods described may be made by those skilled in the art without departing from the spirit of the present invention. For example, it is expressly intended that all combinations of those elements and/or method steps which perform substantially the same function in substantially the same way to achieve the same results are within the scope of the invention. Substitutions of elements from one described embodiment to another are also fully intended and contemplated. It is also to be understood that the drawings are not necessarily drawn to scale but that they are merely conceptual in nature. It is the intention, therefore, to be limited only as indicated by the scope of the claims appended hereto.--.

Page 15, line 1, delete "CLAIMS" and insert therefor --What is claimed is:--.

IN THE CLAIMS:

Please amend 3, 5, 6, 8, 10 to 15 to read as follows:

a2

3. A synchroniser as claimed in claim 1, wherein said providing means, said controlling means and said estimating means are in the digital domain.

5. A synchroniser as claimed in claim 1, wherein said providing means comprises a digital corrector.

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6. A synchroniser as claimed in claim 1, wherein a rough correction is provided by said control signal.

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8. A synchroniser as claimed in claim 6, wherein a finer correction is provided.

10. A synchroniser as claimed in claim 1, wherein said estimator is arranged to determine that the difference between two successive levels has increased if a difference between the upper of said levels and an estimated level for an actual signal provides a signal at a higher level than a signal provided by a difference between a lower of said levels and an estimated level for the actual signal.

a 5  
11. A synchroniser as claimed in claim 2, wherein said estimator is arranged to determine that the difference between two successive levels has increased if a difference between the upper of said levels and an estimated level for an actual signal provides a signal at a higher level than a signal provided by a difference between a lower of said levels and an estimated level for the actual signal.

12. A synchroniser as claimed in claim 1, wherein said estimator is arranged to determine that an actual signal has changed if a differences between the upper of said levels and an actual signal provides a signal at substantially the same level as a signal provided by a difference between a lower of said levels and the actual signal, said same level being different to a previous level for said actual signal.

13. A synchroniser as claimed in claim 1, wherein said synchroniser is arranged to at least one of acquire and track frequency error.

14. A synchroniser as claimed in claim 1, wherein said synchroniser is arranged to  
at least one of acquire and track timing error.

15. A receiver comprising a synchroniser as claimed in claim 1.

Add the following new claims:

17. A synchroniser as claimed in claim 2, wherein said providing means, said  
controlling means and said estimating means are in the digital domain.

18. A synchroniser as claimed in claim 7, wherein a finer correction is provided.